

What is claimed is:

1. A channel memory device in a broadcasting signal processor,
2. comprising:
3. ²⁰⁰ a key input for inputting a ~~predetermined~~ channel number according to a
4. user's selection;
5. a tuner for tuning to a channel corresponding to the channel number selected
6. by the key input, among received broadcasting signals;
7. ^{said} a signal processor for processing a composite video signal of a channel
8. tuned and output from the tuner;
9. a memory for storing the ~~predetermined~~ channel number; and
10. a controller for receiving the channel number output from the key input and
11. storing the channel number output from the key input in the memory while
12. controlling the tuner to tune to a broadcasting channel corresponding to the
13. received channel number.

2. ^A The channel memory device according to claim 1, wherein the
controller receives the signal output from the signal processor, determines whether
a broadcasting signal is present in the currently tuned broadcasting channel and,
stores the corresponding broadcasting channel number in the memory only if ^{said}
broadcasting signal is present.

3. The channel memory device according to claim 1, further comprising:
200 a character signal generator for generating a character signal for ^{indicating}
^{memorization} ²¹⁰ memory of the ~~predetermined~~ channel number selected by the key input;
a mixer for mixing a signal output from the signal processor with a signal
output from the character signal generator; and
a display for displaying a signal output from the mixer.

4. The channel memory device according to claim 3, wherein the
controller controls the character signal generator to generate a current broadcasting
channel number and a character signal of "memory display" so that a user can
easily identify the memorized broadcasting channel.